
Dsp Vlsi Question Papers

VLSI Logic Synthesis and Design

1990 International Conference on Acoustics, Speech and Signal Processing, April 3-6, 1990, Albuquerque Convention Center, Albuquerque, New Mexico, USA

Digest of Technical Papers

XIII SBMicro, International Conference on Microelectronics and Packaging, ICMP'98:

Advanced courses and invited papers

Design and Implementation

ICASSP 87

December 1-4, 1992, Arlington, Texas

Core-based Behavioral Synthesis

September 5-7, 1990, Princeton, New Jersey

1988 International Conference on Acoustics, Speech, and Signal Processing, April 11-14, 1988, New York Hilton, New York City, USA

December 3-6, 1985, Sheraton Harbor Island Hotel, San Diego, California

Dunfey City Line Hotel, Philadelphia, PA, May 4-7, 1987

1987 IEEE International Symposium on Circuits and Systems

Proceedings of the International Conference on Application Specific Array Processors

Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology

ICASSP 88

VLSI Digital Signal Processing Systems

Government Reports Announcements & Index

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ICASSP 88: A & U, audio & electroacoustics, underwater signal processing

The Impact of Very High Performance Integrated Circuits on Radar, Guidance and Avionics Systems

High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures

Digital Signal Processing

SOC Design Methodologies

1987 IEEE International Solid-State Circuits Conference

Electrical & Electronics Abstracts

Proceedings of Technical Papers : April 18-20, 2001, Lakeshore Hotel, Hsinchu, Taiwan, ROC.

Digital Signal Processing

Principles, Devices and Applications

Conference Record

ICASSP 88: V & E, VLSI, spectral estimation

Transputer Applications

Proceedings, Real-Time Systems Symposium

2001 International Symposium on VLSI Technology, Systems, and Applications

Progress and Prospects : Proceedings of the Closing Symposium of the SERC/DTI

Initiative in the Engineering Applications of Transputers

ICASSP 90

IFIP TC10 / WG10.5 Eleventh International Conference on Very Large Scale Integration of Systems-on-Chip (VLSI-SOC'01) December 3-5, 2001, Montpellier, France

Logic and Architecture Synthesis

Papers Presented November 5-7, 1990, Pacific Grove, California

Proceedings of Technical Papers

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LAWRENCE ALEXANDER

VLSI Logic Synthesis and Design los
PressInc

Designing VLSI systems represents a challenging task. It is a transfonnation among different specifications corresponding to different levels of design: abstraction, behavioral, stntctural and physical. The behavioral level describes the functionality of the design. It consists of two components; static and dynamic. The static component describes operations, whereas the dynamic component describes sequencing and timing. The structural level contains infonnation about components, control and connectivity. The physical level describes the constraints that should be imposed on the floor plan, the placement of components, and the geometry of the design. Constraints of area, speed and power are also applied at this level. To implement such multilevel transfonnation, a design methodology should be devised, taking into consideration the constraints, limitations and properties of each level. The mapping process between any of these domains is non-isomorphic. A single behavioral component may be transfonned into more than one structural component. Design methodologies are the most recent evolution in the design automation era,

which started off with the introduction and subsequent usage of module generation especially for regular structures such as PLA's and memories. A design methodology should offer an integrated design system rather than a set of separate unrelated routines and tools. A general outline of a desired integrated design system is as follows: * Decide on a certain unified framework for all design levels. * Derive a design method based on this framework. * Create a design environment to implement this design method.

1990 International Conference on Acoustics, Speech and Signal Processing, April 3-6, 1990, Albuquerque Convention Center, Albuquerque, New Mexico, USA
Springer

Digital audio, speech recognition, cable modems, radar, high-definition television-these are but a few of the modern computer and communications applications relying on digital signal processing (DSP) and the attendant application-specific integrated circuits (ASICs). As information-age industries constantly reinvent ASIC chips for lower power consumption and higher efficiency, there is a growing need for designers who are current and fluent in VLSI design methodologies for DSP. Enter VLSI Digital Signal Processing Systems-a unique, comprehensive guide to performance optimization techniques in VLSI signal processing. Based on Keshab Parhi's highly respected and popular graduate-level courses, this

volume is destined to become the standard text and reference in the field. This text integrates VLSI architecture theory and algorithms, addresses various architectures at the implementation level, and presents several approaches to analysis, estimation, and reduction of power consumption. Throughout this book, Dr. Parhi explains how to design high-speed, low-area, and low-power VLSI systems for a broad range of DSP applications. He covers pipelining extensively as well as numerous other techniques, from parallel processing to scaling and roundoff noise computation. Readers are shown how to apply all techniques to improve implementations of several DSP algorithms, using both ASICs and off-the-shelf programmable digital signal processors. The book features hundreds of graphs illustrating the various DSP algorithms, examples based on digital filters and transforms clarifying key concepts, and interesting end-of-chapter exercises that help match techniques with applications. In addition, the abundance of readily available techniques makes this an extremely useful resource for designers of DSP systems in wired, wireless, or multimedia communications. The material can be easily adopted in new courses on either VLSI digital signal processing architectures or high-performance VLSI system design. An invaluable reference and practical guide to VLSI digital signal processing. A tremendous source of optimization techniques indispensable in modern VLSI signal processing, VLSI Digital Signal Processing Systems promises to become the standard in the field. It offers a rich training ground for students of VLSI design for digital signal processing and provides immediate access to state-of-

the-art, proven techniques for designers of DSP applications-in wired, wireless, or multimedia communications. Topics include: * Transformations for high speed using pipelining, retiming, and parallel processing techniques * Power reduction transformations for supply voltage reduction as well as for strength or capacitance reduction * Area reduction using folding techniques * Strategies for arithmetic implementation * Synchronous, wave, and asynchronous pipelining * Design of programmable DSPs. An Instructor's Manual presenting detailed solutions to all the problems in the book is available from the Wiley editorial department.

Digest of Technical Papers Wiley-IEEE Press

Very Good, No Highlights or Markup, all pages are intact.

XIII SBMicro, International Conference on Microelectronics and Packaging, ICMP'98: Advanced courses and invited papers VLSI Design Methodologies for Digital Signal Processing Architectures Application-specific standard products (ASSPs) and application-specific integrated circuits (ASICs) are expected to become more than fifty percent of the \$10 billion VLSI Digital Signal Processing (DSP) market in year 2000. With rapidly shrinking time-to-market (TTM) requirements, and multiple design goals that seek to optimize sample rate, clock speed, area, and power, the novel core-based behavioral synthesis methodology presented in this book shows how organizations can meet these new challenges effectively and consistently over the next decade. The authors show how VLSI chips can be rapidly designed within a VHDL-based synthesis environment using a pre-designed library of core components. The core library represents synthesizable units of

behavior (function and control) that are both application-specific and organization-specific, empowering the chip designer with a competitive advantage. The key to the quick-turnaround is the high amount of systematic reuse utilized within the design methodology. The percolation of accurate power, speed, area, and timing information to higher levels of abstraction allows rapid and efficient exploration of the design space facilitating the optimization of these objectives individually or concurrently. System integration and test of ASICs into board-level designs is also facilitated. Quick-Turnaround ASIC Design with VHDL: Core-Based Behavioral Synthesis presents a new approach to behavioral synthesis that uses a pre-designed library of DSP cores, providing a highly competitive alternative to existing high-level synthesis tools for DSP.

Design and Implementation Springer Science & Business Media

Devices overview. Discrete signal and systems. Z transforms. The discrete Fourier transform. FIR and IIR filter design methods. Kalman filters. Implementation of digital control algorithms. Review of architectures. Microcontrollers. Systolic arrays. Case studies.

ICASSP 87 IET

VLSI 2010 Annual Symposium will present extended versions of the best papers presented in ISVLSI 2010 conference. The areas covered by the papers will include among others: Emerging Trends in VLSI, Nanoelectronics, Molecular, Biological and Quantum Computing. MEMS, VLSI Circuits and Systems, Field-programmable and Reconfigurable Systems, System Level Design, System-on-a-Chip Design, Application-Specific

Low Power, VLSI System Design, System Issues in Complexity, Low Power, Heat Dissipation, Power Awareness in VLSI Design, Test and Verification, Mixed-Signal Design and Analysis, Electrical/Packaging Co-Design, Physical Design, Intellectual property creating and sharing.

December 1-4, 1992, Arlington, Texas Springer Science & Business Media

This textbook and reference for graduate level courses in digital signal processing can be used in a variety of courses. It includes details about deterministic signal processing, algorithms for convolution and DFT, multirate DSP, digital filter banks, wavelets and multiresolution analysis.

Core-based Behavioral Synthesis Wiley-Interscience

The early era of neural network hardware design (starting at 1985) was mainly technology driven. Designers used almost exclusively analog signal processing concepts for the recall mode. Learning was deemed not to cause a problem because the number of implementable synapses was still so low that the determination of weights and thresholds could be left to conventional computers. Instead, designers tried to directly map neural parallelity into hardware. The architectural concepts were accordingly simple and produced the so called interconnection problem which, in turn, made many engineers believe it could be solved by optical implementation in adequate fashion only. Furthermore, the inherent fault-tolerance and limited computation accuracy of neural networks were claimed to justify that little effort is to be spend on careful design, but most effort be put on technology issues. As a result, it was almost impossible to predict

whether an electronic neural network would function in the way it was simulated to do. This limited the use of the first neuro-chips for further experimentation, not to mention that real-world applications called for much more synapses than could be implemented on a single chip at that time. Meanwhile matters have matured. It is recognized that isolated definition of the effort of analog multiplication, for instance, would be just as inappropriate on the part of the chip designer as determination of the weights by simulation, without allowing for the computing accuracy that can be achieved, on the part of the user.

September 5-7, 1990, Princeton, New Jersey Springer Science & Business Media

VLSI Design Methodologies for Digital Signal Processing Architectures Springer Science & Business Media

1988 International Conference on Acoustics, Speech, and Signal Processing, April 11-14, 1988, New York Hilton, New York City, USA Springer Science & Business Media

The 11 th IFIP International Conference on Very Large Scale Integration, in Montpellier, France, December 3-5, 2001, was a great success. The main focus was about IP Cores, Circuits and System Designs & Applications as well as SOC Design Methods and CAD. This book contains the best papers (39 among 70) that have been presented during the conference. Those papers deal with all aspects of importance for the design of the current and future integrated systems. System on Chip (SOC) design is today a big challenge for designers, as a SOC may contain very different blocks, such as microcontrollers, DSPs, memories including embedded DRAM, analog, FPGA, RF front-ends for wireless

communications and integrated sensors. The complete design of such chips, in very deep submicron technologies down to 0.13 μm , with several hundreds of millions of transistors, supplied at less than 1 Volt, is a very challenging task if design, verification, debug and industrial test are considered. The microelectronic revolution is fascinating; 55 years ago, in late 1947, the transistor was invented, and everybody knows that it was by William Shockley, John Bardeen and Walter H. Brattain, Bell Telephone Laboratories, which received the Nobel Prize in Physics in 1956. Probably, everybody thinks that it was recognized immediately as a major invention.

December 3-6, 1985, Sheraton Harbor Island Hotel, San Diego, California Springer

This book describes several methods and systems solving one of the highlighted problems within computer aided design, namely architectural and logic synthesis. The book emphasises the most recent technologies in high level synthesis, concentrating on applicative studies and practical constraints or criteria during synthesis. Logic and Architecture Synthesis concentrates on the practical problems involving automatic synthesis of designs. It is essential reading for researchers and CAD Managers working in this area.

Dunfey City Line Hotel, Philadelphia, PA, May 4-7, 1987 Springer Science & Business Media

Electrical Engineering/Signal Processing High—Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two-volume set, High-Performance VLSI Signal Processing: Innovative Architectures and Algorithms brings together the most innovative papers in

the field, focused introductory material, and extensive references. The editors present timely coverage of algorithm and design methodologies with an emphasis on today's rapidly-evolving high-speed architectures for VLSI implementations. These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high-performance VLSI processing. The editors provide a practical understanding of the merits of total system design through an insightful, synergistic presentation of methodology, architecture, and infrastructure. Each volume features: Major papers that span the wide range of research areas in the field Chapter introductions, including historical perspectives Numerous applications-oriented design examples Coverage of current and future technological trends Thorough treatment of high-speed architectures

1987 IEEE International Symposium on Circuits and Systems Cambridge University Press

The symposium held in Reading in March 1992 celebrated the completion of a 5-year Initiative in the Engineering Applications of Transputers. It reviewed achievements in a range of applications and supporting fields and predicted future developments. This book represents a collection of articles presented at this meeting, as well as independent reviews of the Transputer Initiative.

Proceedings of the International Conference on Application Specific Array Processors Prentice Hall

Digital signal processing lies at the heart of the communications revolution and is an essential element of key technologies such as mobile phones and the Internet. This book covers all the major topics in

digital signal processing (DSP) design and analysis, supported by MatLab examples and other modelling techniques. The authors explain clearly and concisely why and how to use digital signal processing systems; how to approximate a desired transfer function characteristic using polynomials and ratio of polynomials; why an appropriate mapping of a transfer function on to a suitable structure is important for practical applications; and how to analyse, represent and explore the trade-off between time and frequency representation of signals. An ideal textbook for students, it will also be a useful reference for engineers working on the development of signal processing systems.

Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology IOS Press

Synthesis and Optimization of DSP Algorithms describes approaches taken to synthesising structural hardware descriptions of digital circuits from high-level descriptions of Digital Signal Processing (DSP) algorithms. The book contains: -A tutorial on the subjects of digital design and architectural synthesis, intended for DSP engineers, -A tutorial on the subject of DSP, intended for digital designers, -A discussion of techniques for estimating the peak values likely to occur in a DSP system, thus enabling an appropriate signal scaling. Analytic techniques, simulation techniques, and hybrids are discussed. The applicability of different analytic approaches to different types of DSP design is covered, -The development of techniques to optimise the precision requirements of a DSP algorithm, aiming for efficient implementation in a custom parallel processor. The idea is to trade-off numerical accuracy for area or

power-consumption advantages. Again, both analytic and simulation techniques for estimating numerical accuracy are described and contrasted. Optimum and heuristic approaches to precision optimisation are discussed, -A discussion of the importance of the scheduling, allocation, and binding problems, and development of techniques to automate these processes with reference to a precision-optimized algorithm, -Future perspectives for synthesis and optimization of DSP algorithms.

ICASSP 88

VLSI Digital Signal Processing Systems

Government Reports

Announcements & Index

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